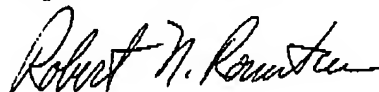


74. (New) A method as in claim 68, comprising the step of encoding the plurality of signals, wherein the plurality of signals are encoded differently for each respective antenna of the plurality of transmit antennas.

REMARKS

Claims 1-74 are pending. New claims 60-74 have been added. Applicants respectfully request examination and allowance of claims 1-74. If Examiner finds any issue that is unresolved, please call applicants' attorney by dialing the telephone number printed below.

Respectfully submitted,



Robert N. Rountree  
Reg. No. 39,347  
Attorney for Applicants

Texas Instruments Incorporated  
P. O. Box 655474, M/S 3999  
Dallas, Texas 75265  
(972) 917-4431  
Fax: (972) 917-4418

**Marked Up Amendment****In the Specification:**

Rewrite the paragraph at page 18, line 15 as follows.

[0037] Figure 3 illustrates an electrical and functional block diagram of a multi-input multi-output ("MIMO") system 70 according to one preferred embodiment. As Figure 3 represents an electrical and functional block diagram, one skilled in the art may ascertain various technical manners of implementing system 70 using a combination of hardware and software, including the preferred [used] use of a digital signal processor to include at least some of the illustrated blocks. Turning then to system 70, it includes a transmitter 72 and a receiver 74. Transmitter 72 and receiver 74 in some respects are comparable to system 10 described earlier; thus, to simplify the present discussion the comparable blocks are shown with a like reference identifier, with the addition of an apostrophe to the identifier in Figure 3 so that certain blocks may be referred to with respect to system 70 without causing a confusing reference as to the earlier prior art system 10. Additionally, the comparable blocks are discussed to a lesser extent below given the previous discussion as well as the skill in the art. Finally, as a multiple antenna system, in the illustrated example two transmit and two receive antennas are illustrated. However, the present inventive teachings may be extended to various systems with  $P$  transmit antennas and  $Q$  receive antennas.

Rewrite the paragraph at page 24, line 3 as follows.

[0045] Compute combined effective channel block 86 determines the product of the matrices it receives from blocks 82 and 84 (unless that product was already determined [by] due to the inclusion of the pilot and data in the same channel as discussed above with respect to channel estimator 82). To perform this operation, recall that  $V^{(n)}$  basis selector block 84 communicates to block 86 each value of  $V^{(n)}$  and, therefore, this includes the immediately previous value of  $V^{(n)}$  that it fed back to transmitter 72 (i.e.,  $V^{(n)}(t-1)$ ). In response, block 86 determines the product of  $\hat{H}$  and  $V^{(n)}(t-1)$ , where the designation of this product is simplified in the remaining discussion by

dropping the temporal aspect of  $(t-1)$  and hence, is referred to as  $(\hat{\mathbf{H}} \mathbf{V}^{(n)})$ . Further, this product, or its conjugate transpose,  $(\hat{\mathbf{H}} \mathbf{V}^{(n)})^H$ , is output by block 86 to matched filter 80, which uses this product to solve for the vector  $\underline{y}$  in Equation 27 described above.

Rewrite the paragraph of Equation 37 at page 27, line 17 as follows.

$$\phi_m = \frac{2\pi}{[2]M} (m-1) \quad , \quad m=1,2,\dots,M \quad \text{Equation 37}$$

Rewrite the paragraph at page 33, line 13 as follows.

[0060] Figure 4 illustrates an electrical block diagram of a double space time block coded transmit antenna diversity ("DSTTD") system 100 according to another preferred embodiment. Like Figure 3, since Figure 4 represents an electrical and functional block diagram, one skilled in the art may ascertain various technical manners of implementing system 100 into a combination of hardware and software, including the preferred [used] use of a digital signal processor to include at least some of the illustrated blocks. Turning then to system 100, and by way of introduction, STTD technology in general is known in the art. Indeed, for further aspects that could be incorporated in such a system, the reader is invited to review the following two patent applications, both of which are hereby incorporated herein by reference: (i) U.S. Patent Application 09/578,004 (docket TI-29286), filed May 24, 2000; and (ii) U.S. Patent Application 09/885,878 (docket TI-31293), filed June 20, 2001. In system 100, it includes a transmitter 102 and a receiver 104. In some respects, transmitter 102 and receiver 104 are comparable to transmitter 72 and receiver 74 of system 70 described earlier and, thus, to simplify the present discussion the comparable blocks are shown with a like reference identifier and are generally not discussed once more so as to simplify the remaining discussion. DSTTD system 100 is also a multiple antenna system with  $P$  transmit antennas and  $Q$  receive antennas, where in such a system  $Q$  can be larger than  $P$ ; in the example of Figure 4, therefore,  $P=4$  so that transmit antennas  $\text{TAT}_1''$  through  $\text{TAT}_4''$  are shown, and  $Q$  is a larger number so that receive antennas  $\text{RAT}_1''$  through  $\text{RAT}_Q$  are shown.